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09/994146

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Class	Subclass

PATENT NUMBER

O.I.P.E. *hlp*
 SCANNED *703* O.A. *AA*

PATENT DATE

APPLICATION NO. 09/994146	CONT/PRIOR F	CLASS 257	SUBCLASS 34.7	ART UNIT 2811	EXAMINER <i>HU</i>
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APPLICANTS

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 Kwang-Il Kim

TITLE

Semiconductor device having silicon-on-insulator structure and method of fabricating the same

PTO-2040
 12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	(Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	(Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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